

FIG. 1

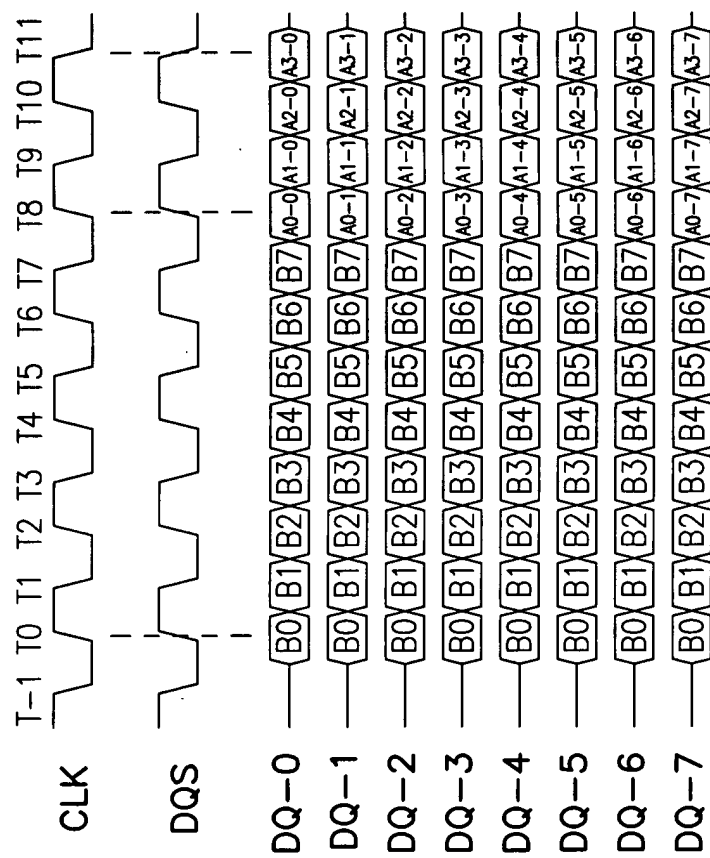


FIG. 2

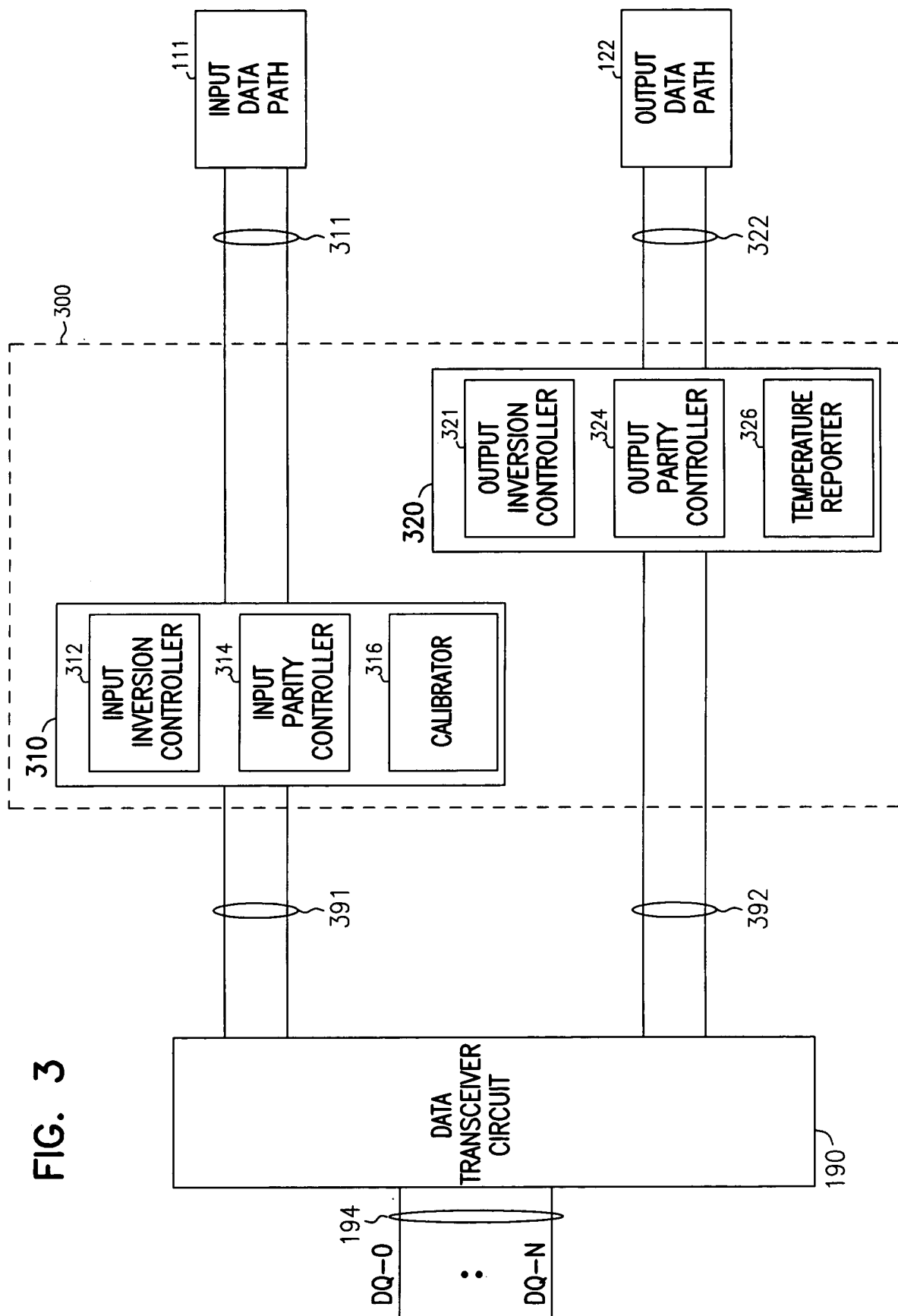


FIG. 3

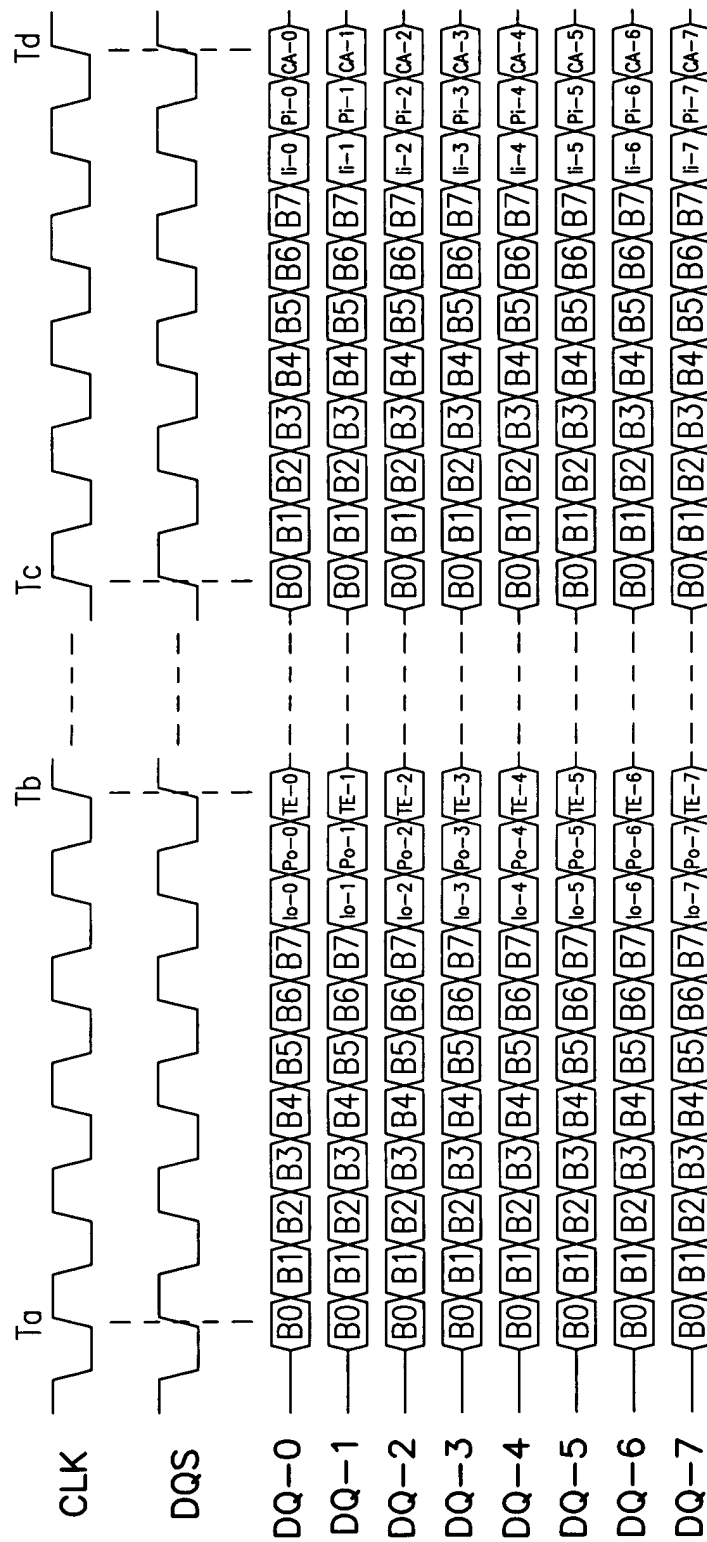


FIG. 4

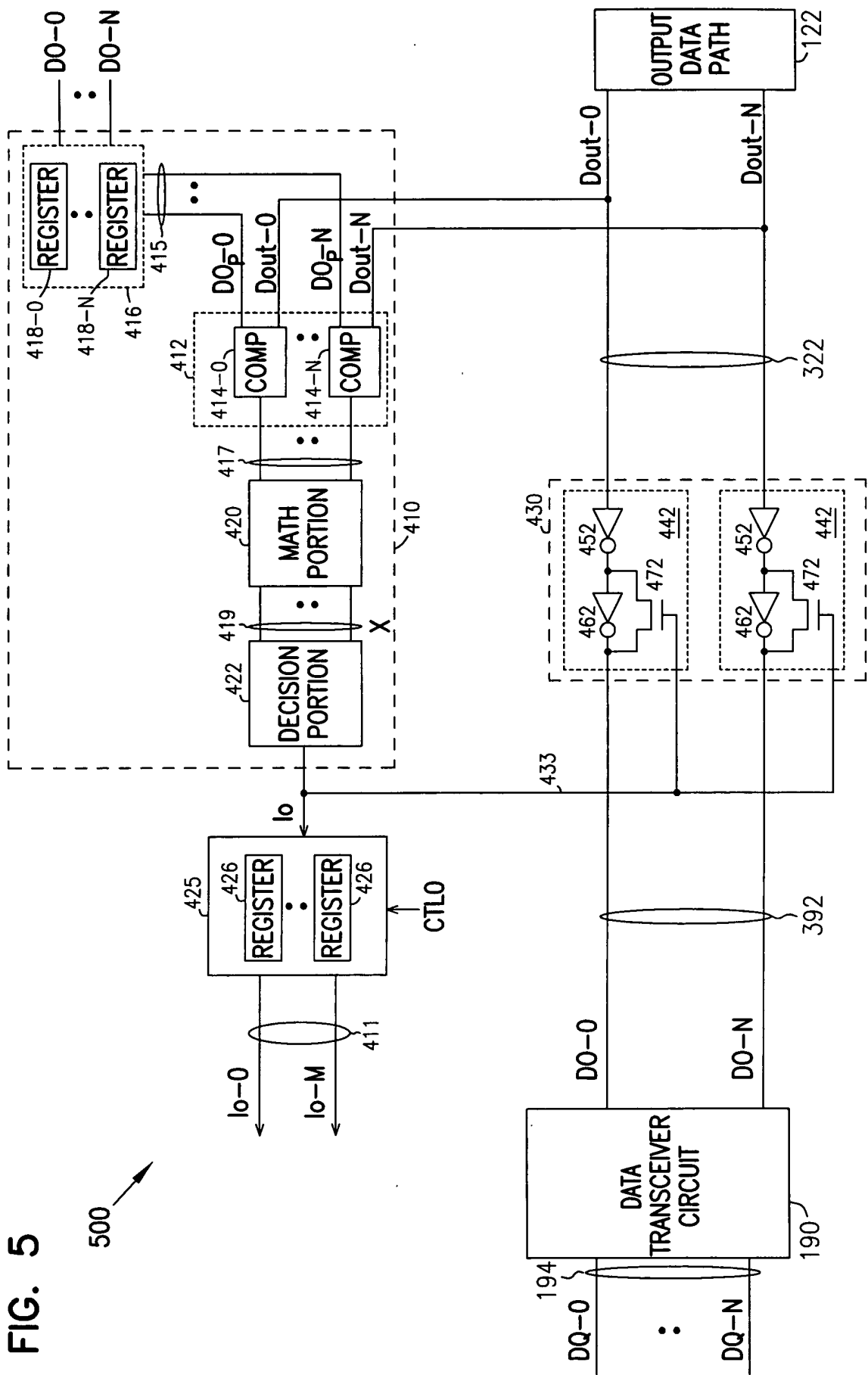


FIG. 5

500

		80 80 80 80 80 80 80 80			
		DO _P		lo	X Y
ROW 1 {	Dout-0 TO Dout-7 INVERTED (Dout-0 TO Dout-7)		00000111		
			11111000 00000111	1	8 0
			11110000 00001111	1	7 1
			11100000 00011111	1	6 2
			11000000 00111111	1	5 3
			10000000 01111111	0	4 4
ROW 6 {	Dout-0 TO Dout-7 INVERTED (Dout-0 TO Dout-7)		00000000 11111111	0	3 5
			00000001 11111110	0	2 6
			00000011 11111100	0	1 7
			00000111 11111000	0	0 8
			00001111 11110000	0	1 7
			00011111 11100000	0	2 6
			00111111 11000000	0	3 5
			01111111 10000000	0	4 4
			11111111 00000000	1	5 3
			11111110 00000001	1	6 2
ROW 16 {	Dout-0 TO Dout-7 INVERTED (Dout-0 TO Dout-7)		11111100 00000011	1	7 1

FIG. 6

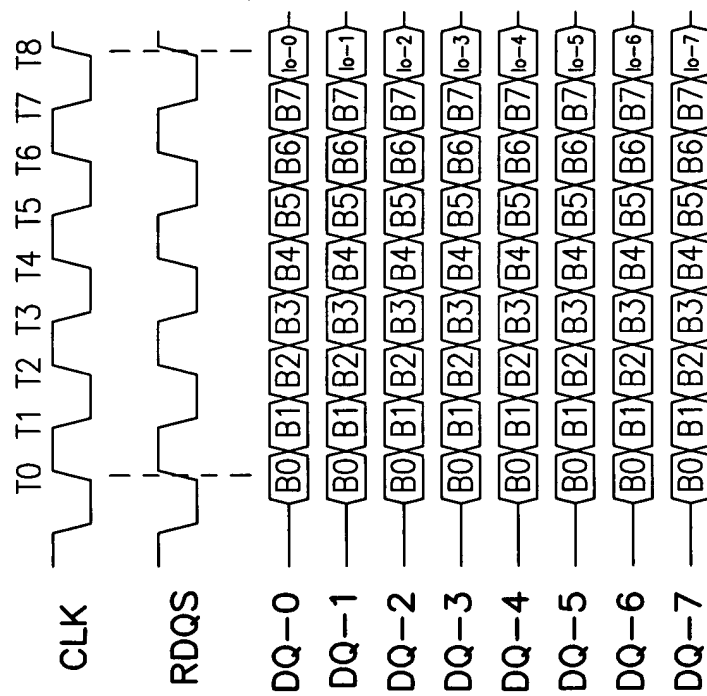


FIG. 7

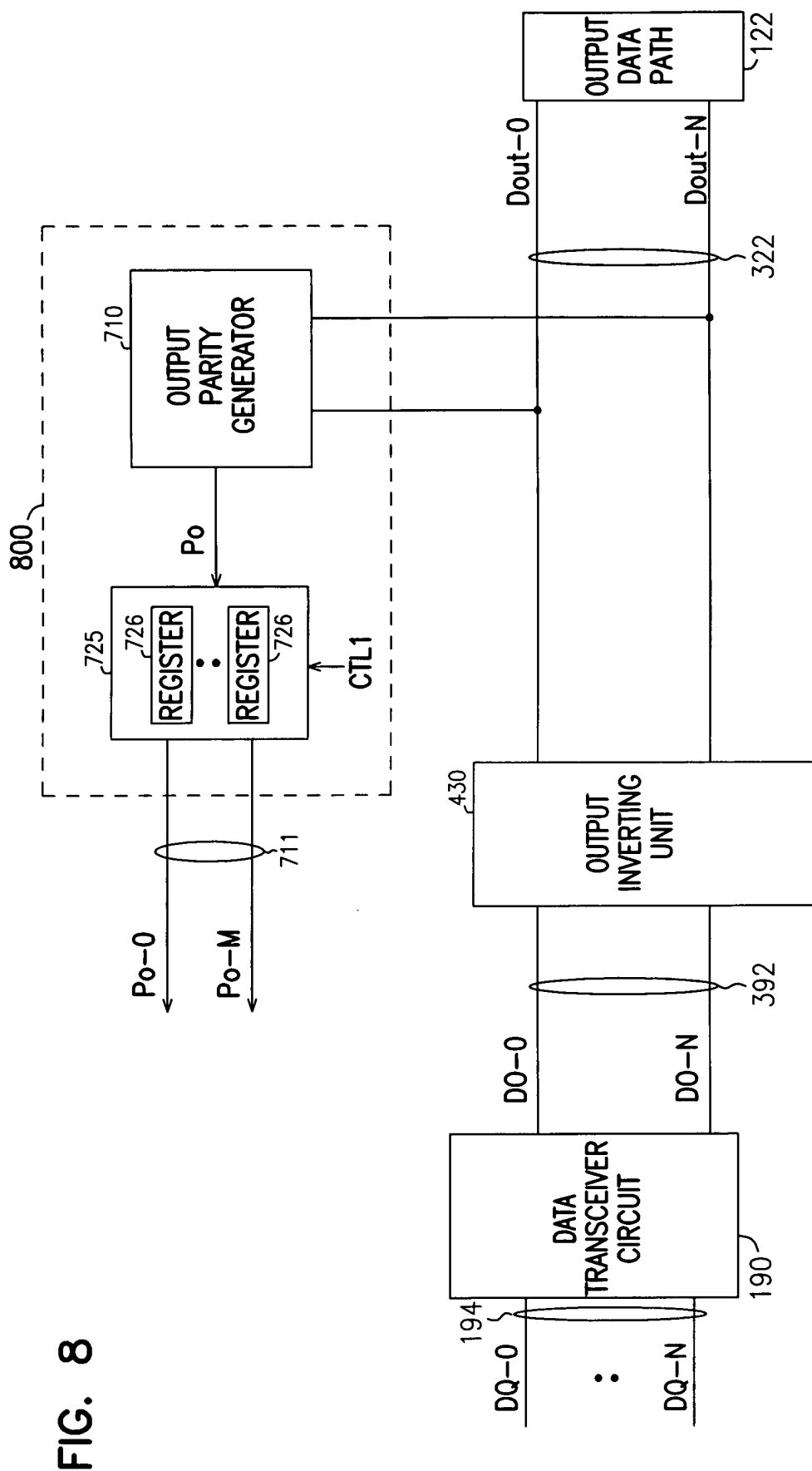


FIG. 8

	Dout-0 TO Dout-7 B0 B0 B0 B0 B0 B0 B0 B0	PO
ROW 1	00000000	0
	00000001	1
ROW 3	00000011	0
	00000111	1
	00001111	0
ROW 6	00011111	1
	00111111	0
	01111111	1
	11111111	0
	11111110	1
	11111100	0
	11111000	1
	11110000	0
ROW 14	11100000	1
	11000000	0
	10000000	1

FIG. 9

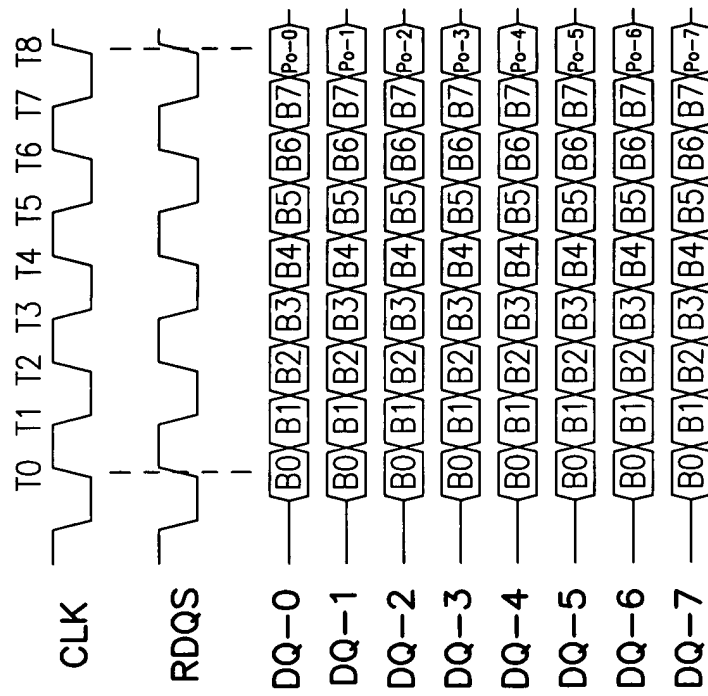
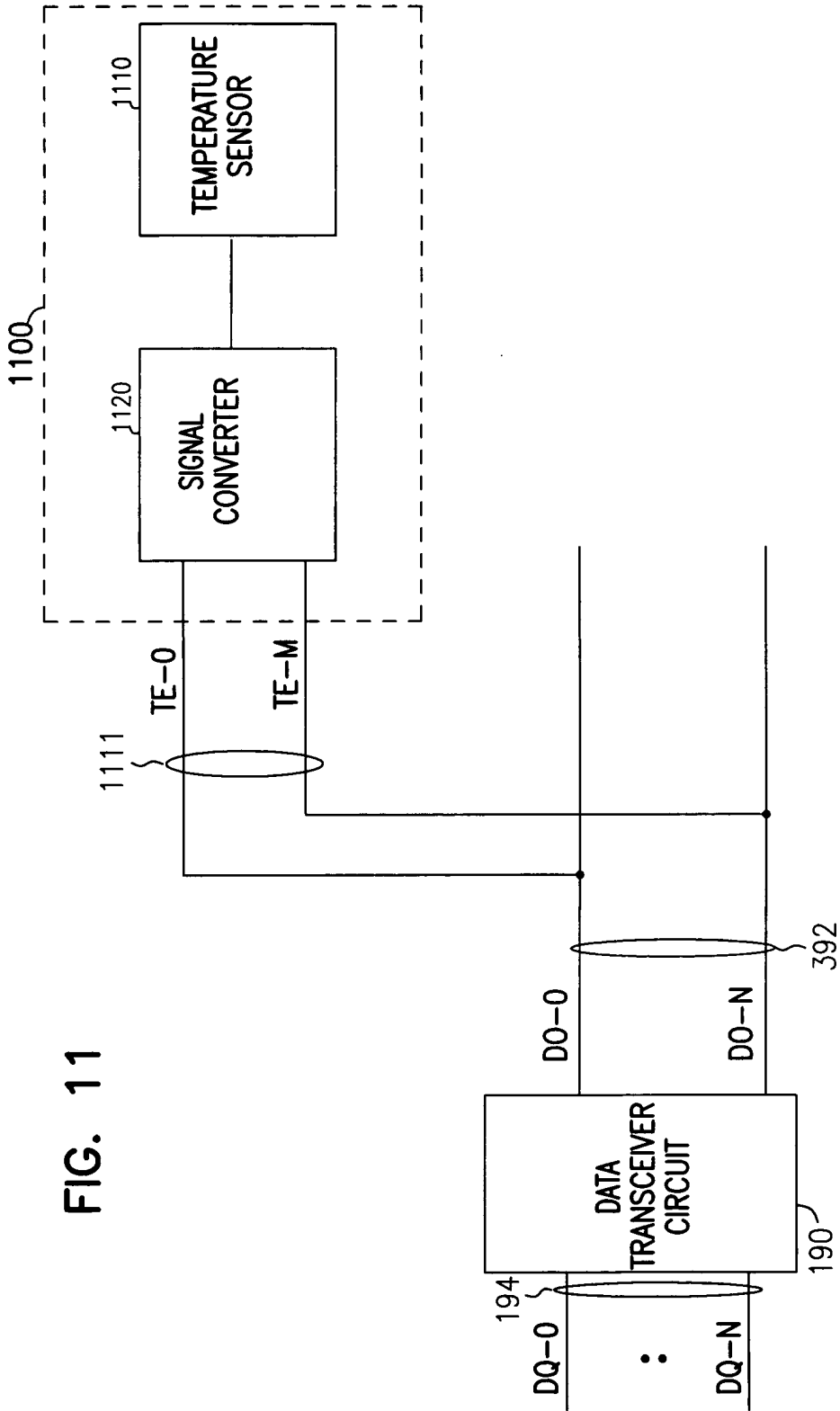


FIG. 10



	COL1	COL2	COL3	COL4	COL5	COL6	COL7	COL8
BITS	TMP	TMP	TMP	TMP	TMP	TMP	TMP	TMP
TE-0	0	0	0	0	0	0	0	0
TE-1	0	0	0	0	0	0	0	1
TE-2	0	0	0	0	0	0	1	1
TE-3	0	0	0	0	0	1	1	1
TE-4	0	0	0	0	1	1	1	1
TE-5	0	0	0	1	1	1	1	1
TE-6	0	0	1	1	1	1	1	1
TE-7	0	1	1	1	1	1	1	1
TEMP °C	0	15	30	45	60	75	90	105

FIG. 12

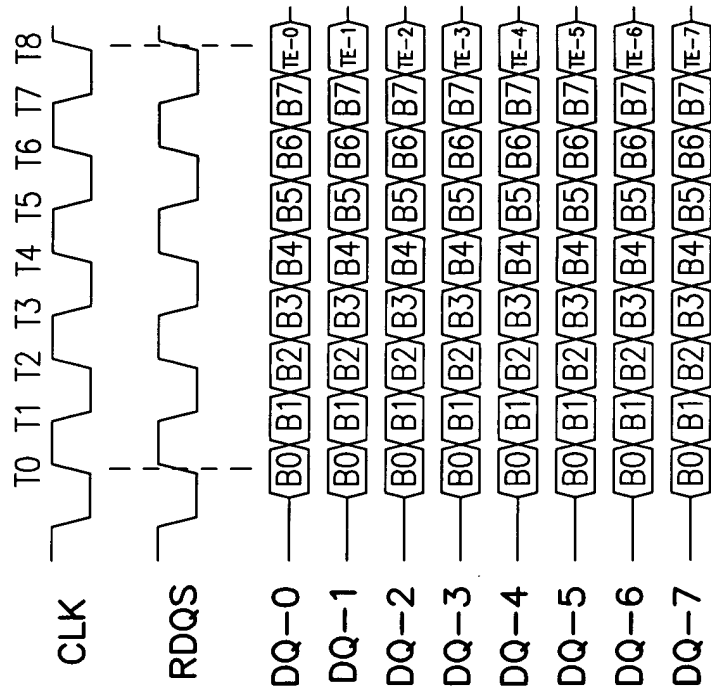
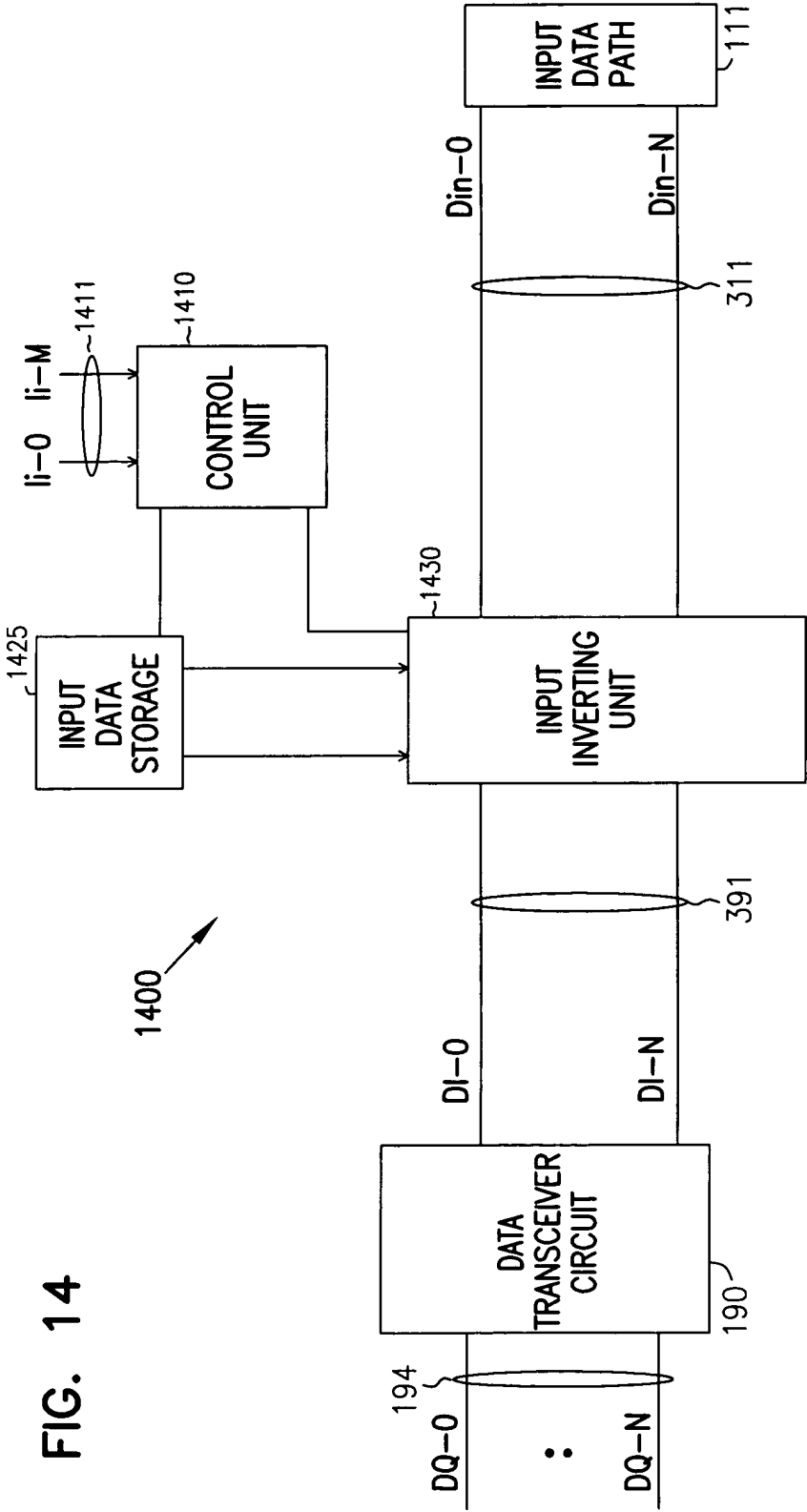


FIG. 13



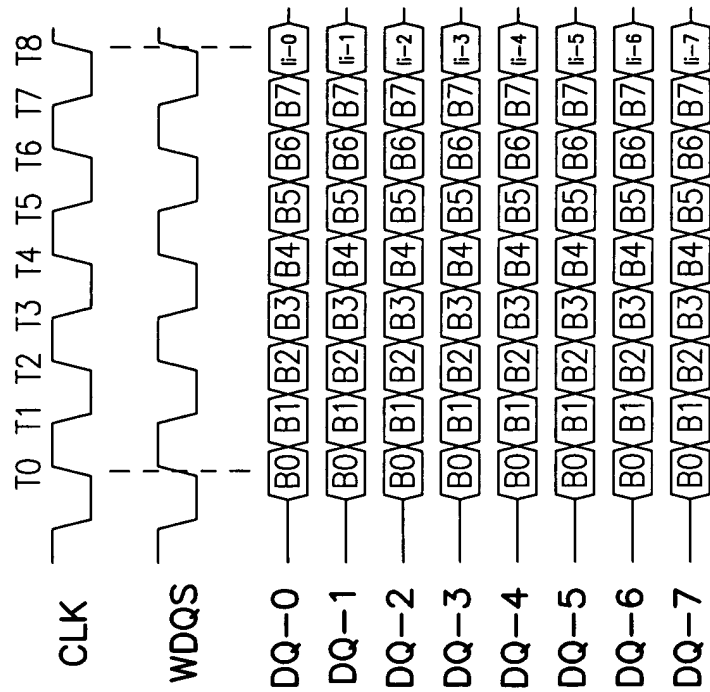


FIG. 15

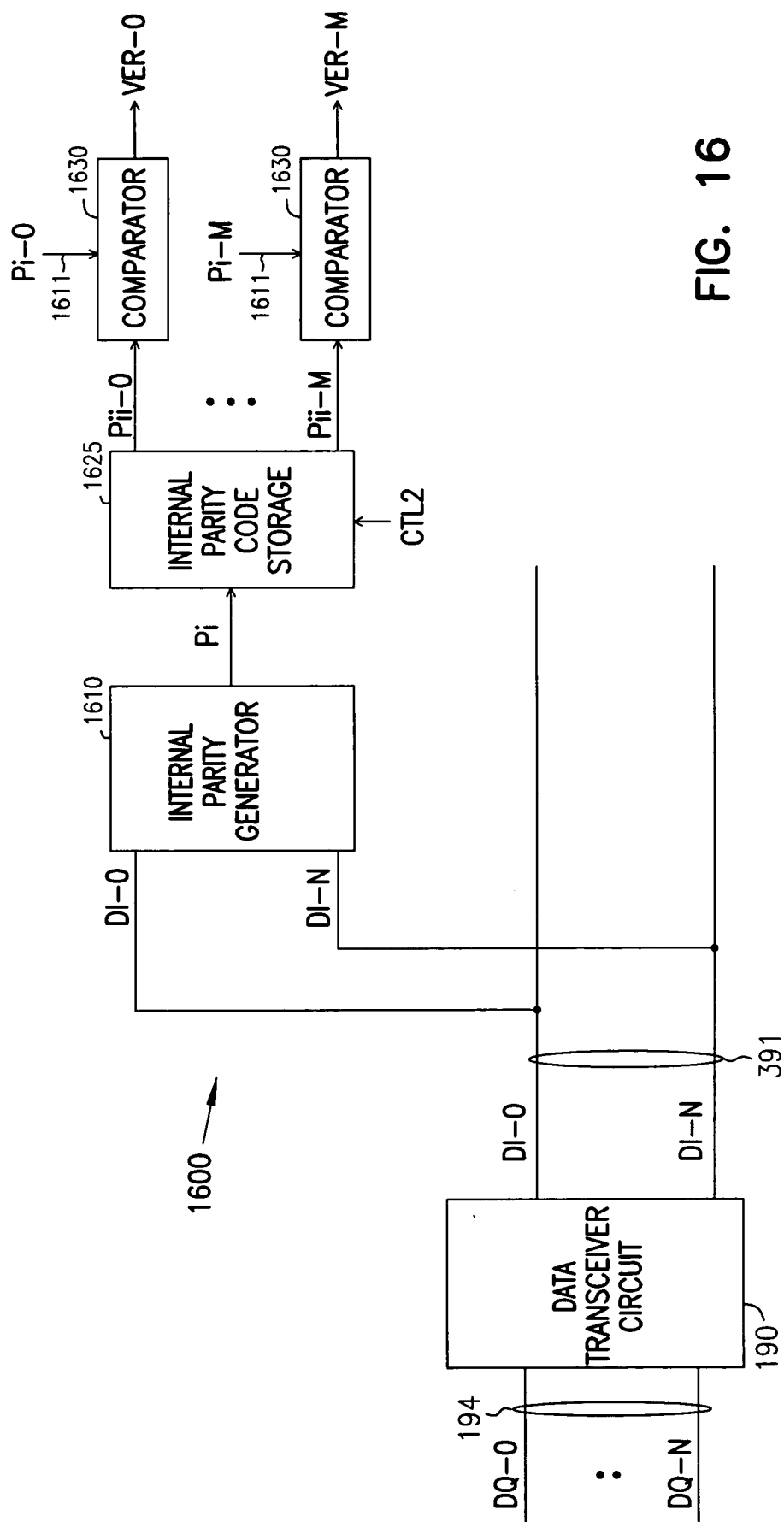


FIG. 16

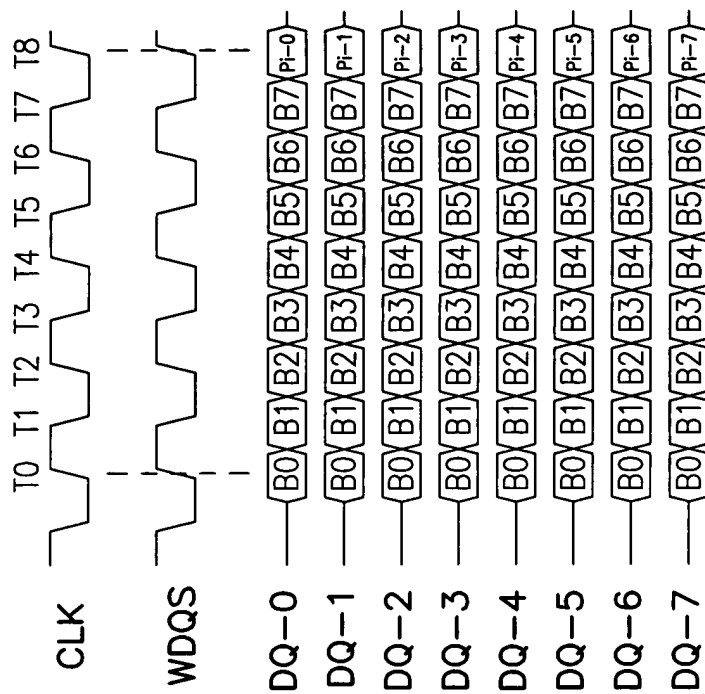


FIG. 17

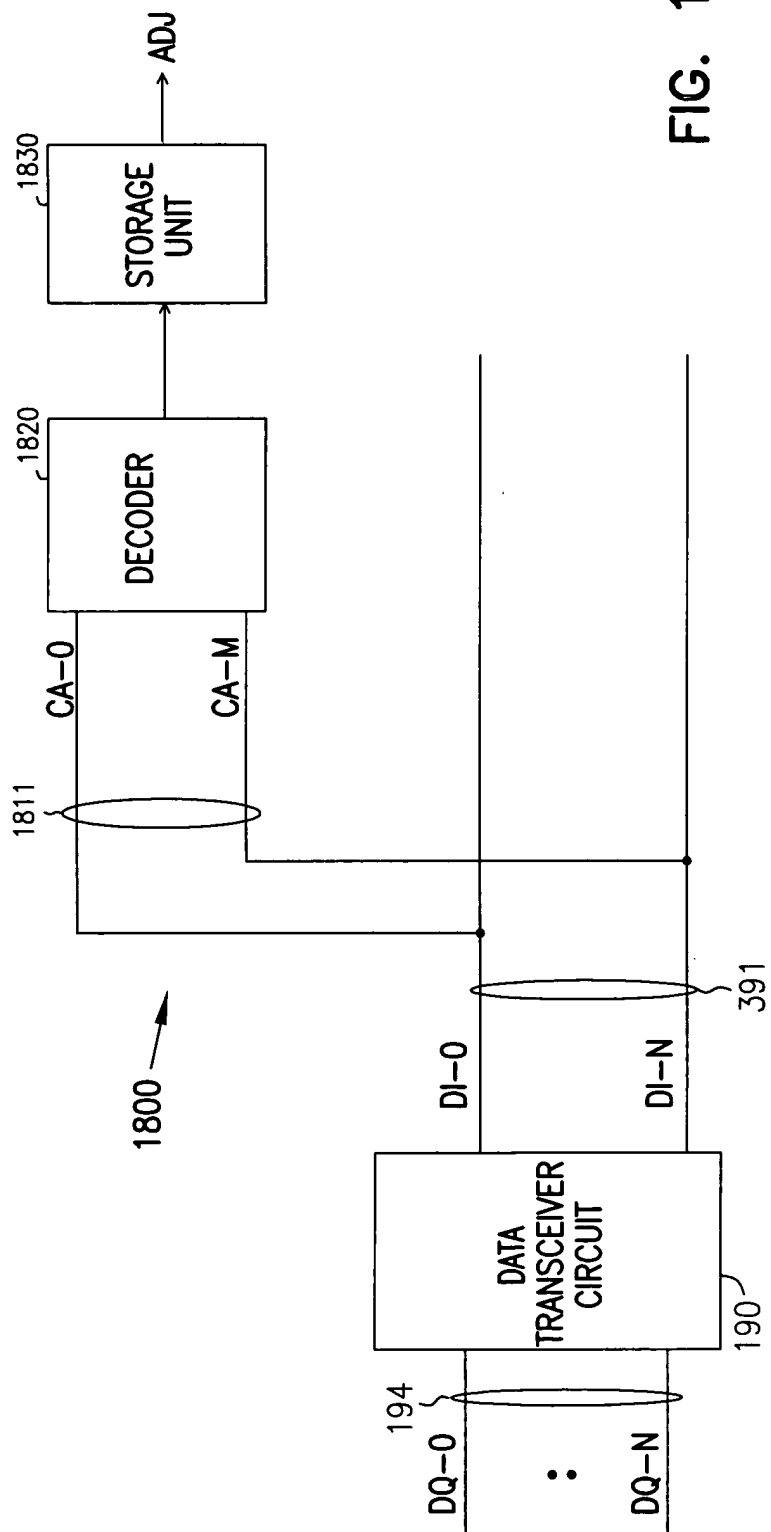


FIG. 18

	COL1	COL2	COL3	COL4	COL5	COL6	COL7	COL8
BITS	CAL	CAL	CAL	CAL	CAL	CAL	CAL	CAL
CA-0	0	0	0	0	0	0	0	0
CA-1	0	0	0	0	0	0	0	1
CA-2	0	0	0	0	0	0	1	1
CA-3	0	0	0	0	0	1	1	1
CA-4	0	0	0	0	1	1	1	1
CA-5	0	0	0	1	1	1	1	1
CA-6	0	0	1	1	1	1	1	1
CA-7	0	1	1	1	1	1	1	1
DELAY	-200	-150	-100	-50	50	100	150	200

FIG. 19

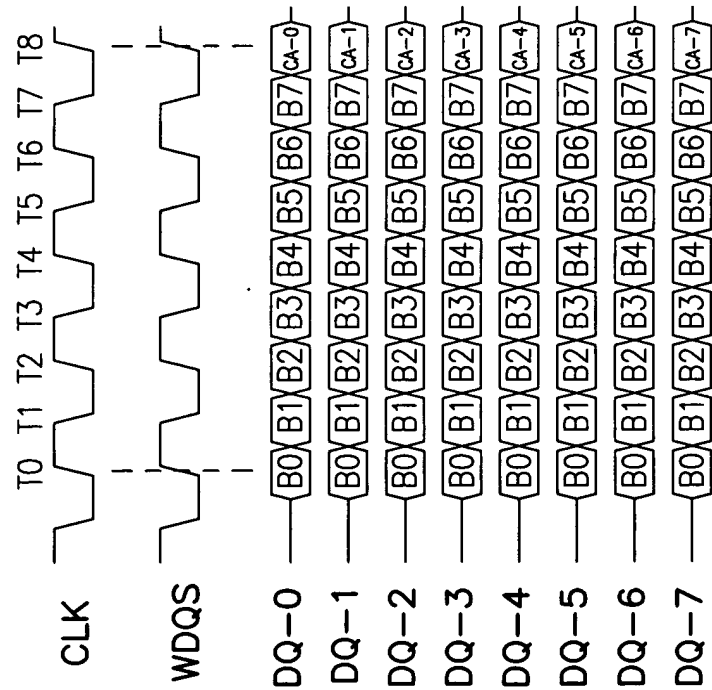


FIG. 20

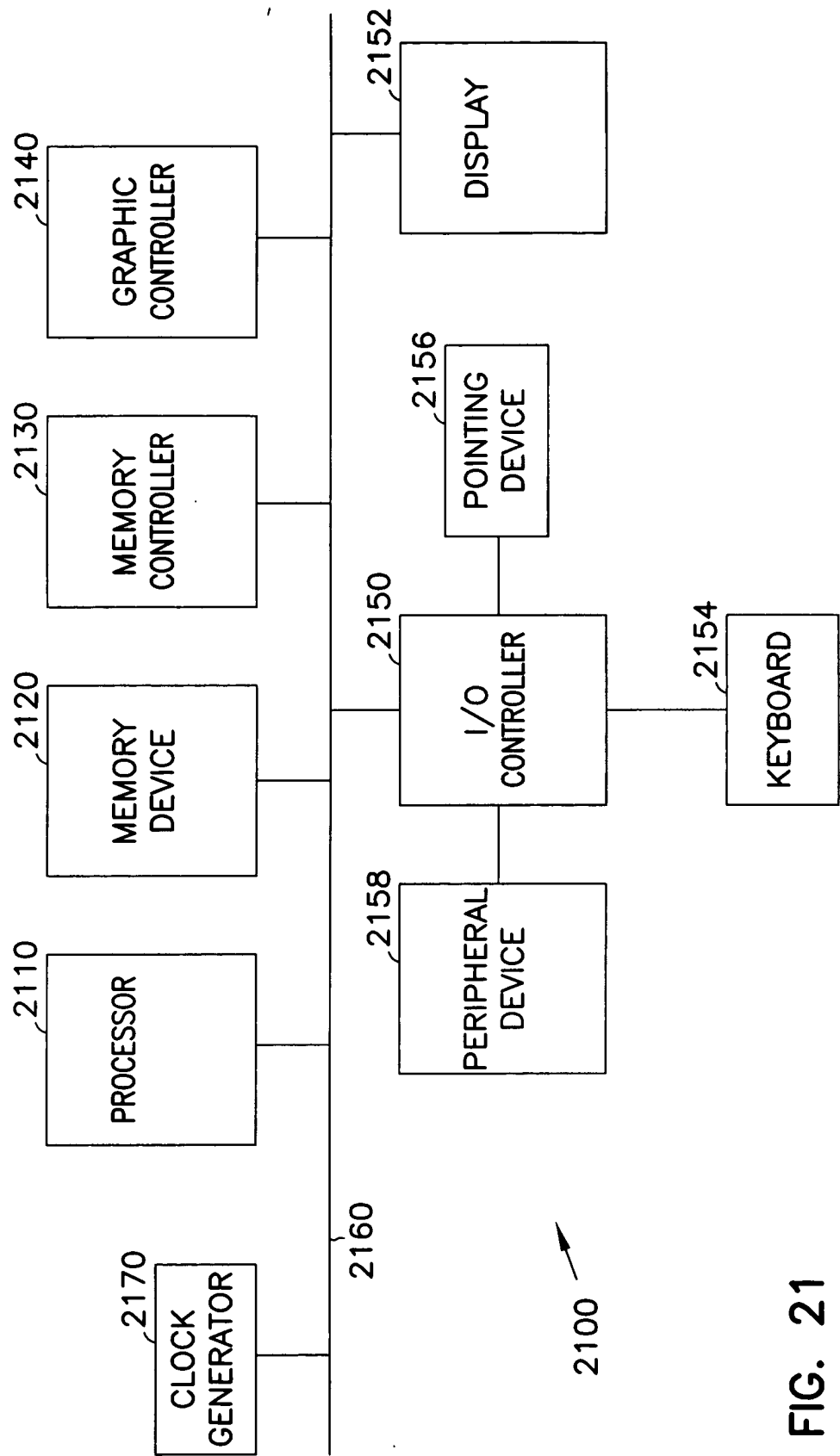


FIG. 21